

### **IN THE SPECIFICATION**

Please amend the paragraph beginning on page 3, line 23 as follows:

As shown in FIG. 6, in this drive circuit, constant current sources IS1 and IS2 are added to the drive circuit shown in FIG. 5. As shown in the figure, the sources of ~~NMOS~~ PMOS transistors QP3 and QP4 are connected to each other and to current source IS1. The supply current  $I_0$  from current source IS1 is input to the sources of transistor QP3 or QP4.